

APPLICATION DATA SHEET

Applicant: 1st Inventor: MELANSON, John Laurence
Residence: 901 West Ninth Street, Apt. 201
Austin, Texas 78703
Mailing: Same as above
Citizenship: United States of America

Correspondence: James J. Murphy, Esq.
Winstead Sechrest & Minick P.C.
P.O. Box 50784
1201 Main Street
Dallas Texas 75250-0784

Application Information: Title: CIRCUITS AND METHODS FOR REDUCING INTERFERENCE FROM
SWITCHED MODE CIRCUITS
Classification: Class: 455 Subclass:
Tech Center: 2683
Drawing Sheets: 3 Drawing to be Published: 1
Docket No: 1067-CA-CA (P108C1)
Type: UTILITY
Secrecy: None

Representative Information: James J. Murphy, Reg. No. 34,503
Steven Lin, Reg. No. 35,250
Scott Thomas, Reg. No. 39,855
Michael J. DeHaemer, Reg. No. 39,164
Barry S. Newberger, Reg. No. 41,527

Express Mail Label No:
EL 859 423 482 US

Domestic Priority:

CLAIM TYPE	SERIAL NO.	FILING DATE	STATUS
Continuation of:	09/651,821	August 30, 2000	Pending

CFR §119 Foreign Priority: NONE

CLAIM TYPE	SERIAL NO.	FILING DATE	STATUS

Assignee:: Name: Cirrus Logic, Inc.
 Address: 2901 Via Fortuna
 Austin, Texas 78746
 United States of America

DALLAS_1\3885569\1
2836-P108US 10/28/2003